N-Channel Trench Power MOSFET

General Description

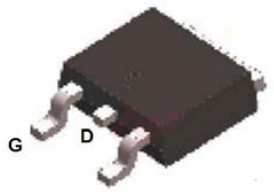
The HM4030D is N-channel MOS Field Effect Transistor designed for high current switching applications. Rugged E_{AS} capability and ultra low $R_{DS(ON)}$ is suitable for PWM, load switching especially for E-Bike controller applications.

Features

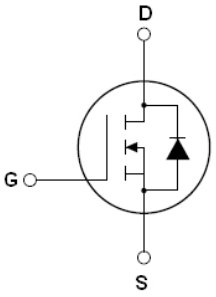
- V_{DS}=100V; I_D=118A@ V_{GS}=10V; $R_{DS(ON)}$<7.5mΩ @ V_{GS}=10V
- Special Designed for E-Bike Controller Application
- Ultra Low On-Resistance
- High UIS and UIS 100% Test

Application

- 72V E-Bike controller applications
- Hard Switched and High Frequency Circuits
- Uninterruptible Power Supply



TO-263-2L top view



Schematic Diagram

Marking and pin assignment

V_{DS} = 100V

I_D= 118A

$R_{DS(ON)}$= 6.2mΩ

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
HM4030D	HM4030D	TO-263-2L	-	-	

Table 1. Absolute Maximum Ratings (TA=25℃)

Symbol	Parameter	Value	Unit
V_{DS}	Drain-Source Voltage (V_{GS}=0V)	100	V
V_{GS}	Gate-Source Voltage (V_{DS}=0V)	±25	V
$I_{D\ (DC)}$	Drain Current (DC) at Tc=25℃	118	A
$I_{D\ (DC)}$	Drain Current (DC) at Tc=100℃	82.6	A
$I_{DM\ (pluse)}$	Drain Current-Continuous@ Current-Pulsed **(Note 1)**	472	A
P_D	Maximum Power Dissipation(Tc=25℃)	230	W
	Derating Factor	1.54	W/℃
E_{AS}	Single Pulse Avalanche Energy **(Note 2)**	960	mJ
T_J,T_{STG}	Operating Junction and Storage Temperature Range	-55 To 175	℃

Notes 1.Repetitive Rating: Pulse width limited by maximum junction temperature

2.E_{AS} condition:T_J=25℃,V_{DD}=50V,V_G=10V, R_G=25Ω

Table 2. Thermal Characteristic

Symbol	Parameter	Value	Unit
R_{JC}	Thermal Resistance,Junction-to-Case	0.65	℃/W

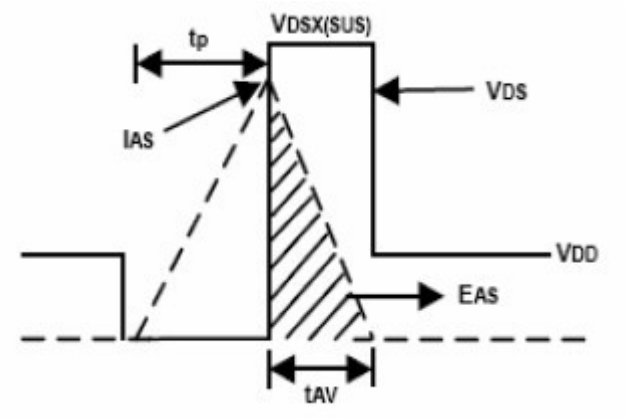
Table 3. Electrical Characteristics (TA=25℃unless otherwise noted)

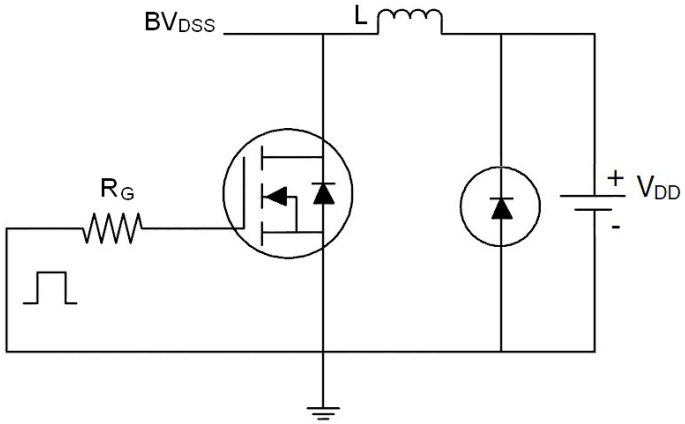
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
On/Off States						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V I_D=250μA	100			V
I_{DSS}	Zero Gate Voltage Drain Current(Tc=25℃)	V_{DS}=100V,V_{GS}=0V			1	μA
I_{DSS}	Zero Gate Voltage Drain Current(Tc=125℃)	V_{DS}=100V,V_{GS}=0V			10	μA
I_{GSS}	Gate-Body Leakage Current	V_{GS}=±20V,V_{DS}=0V			±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=250μA	2		4	V
$R_{DS(ON)}$	Drain-Source On-State Resistance	V_{GS}=10V, I_D=40A		6.2	7.5	mΩ
Dynamic Characteristics						
g_{FS}	Forward Transconductance	V_{DS}=50V,I_D=40A	170			S
C_{iss}	Input Capacitance	V_{DS}=25V,V_{GS}=0V f=1.0MHz		7633		PF
C_{oss}	Output Capacitance			916		PF
C_{rss}	Reverse Transfer Capacitance			513		PF
Q_g	Total Gate Charge	V_{DS}=50V,I_D=40A V_{GS}=10V		139		nC
Q_{gs}	Gate-Source Charge			30.2		nC
Q_{gd}	Gate-Drain Charge			52.1		nC
Switching Times						
$t_{d(on)}$	Turn-on Delay Time	V_{DD}=65V,I_D=40A,R_L=15Ω V_{GS}=10V,R_G=2.5Ω		25		nS
t_r	Turn-on Rise Time			29		nS
$t_{d(off)}$	Turn-Off Delay Time			53		nS
t_f	Turn-Off Fall Time			63		nS
Source-Drain Diode Characteristics						
I_{SD}	Source-Drain Current(Body Diode)			118		A
I_{SDM}	Pulsed Source-Drain Current(Body Diode)			472		A
V_{SD}	Forward On Voltage(Note 1)	T_J=25℃,I_{SD}=40A,V_{GS}=0V		0.85	1	V
t_{rr}	Reverse Recovery Time(Note 1)	T_J=25℃,I_F=40A di/dt=100A/μs		95		nS
Q_{rr}	Reverse Recovery Charge(Note 1)			189		nC
t_{on}	Forward Turn-on Time	Intrinsic turn-on time is negligible(turn-on is dominated by L_S+L_D)				

Notes 1.Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 1.5%, R_G=25Ω, Starting T_J=25℃

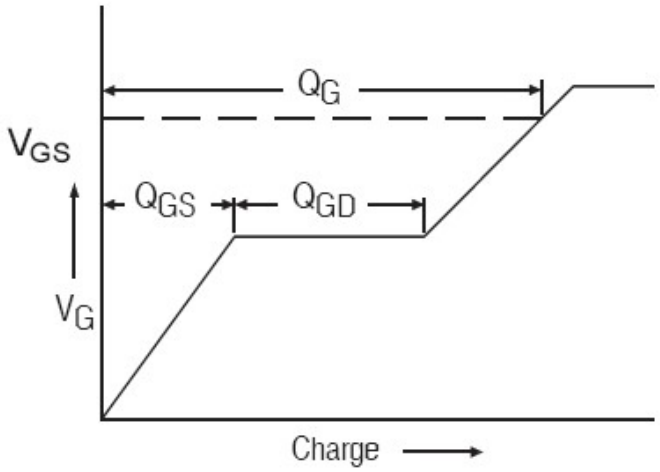
Test Circuit

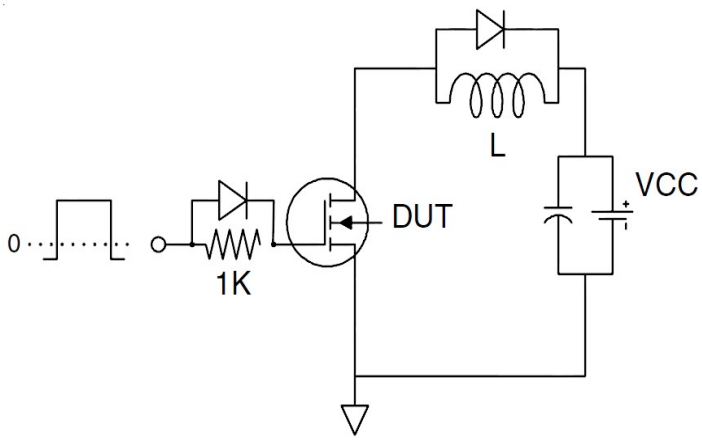
1） E_{AS} Test Circuits



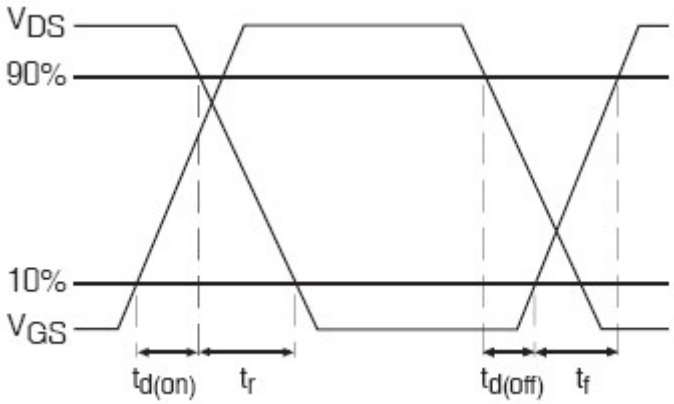


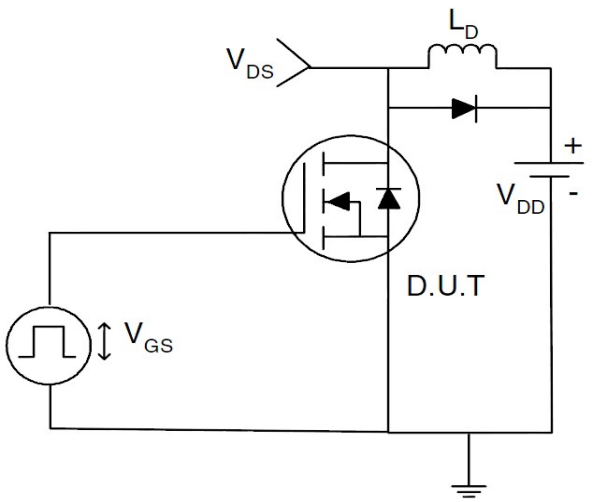
2）Gate Charge Test Circuit:





3）Switch Time Test Circuit：





TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS (Curves)

Figure1. Output Characteristics

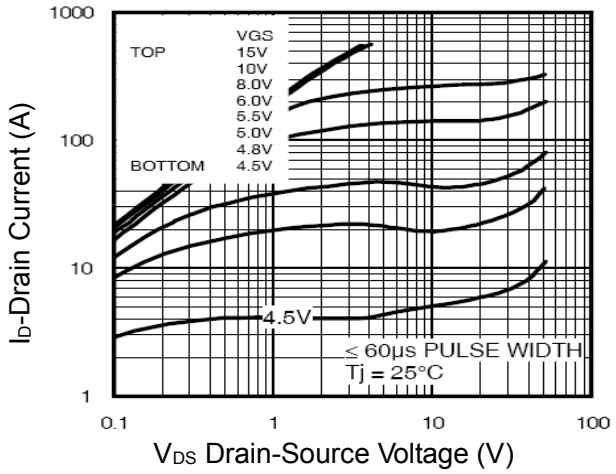


Figure2. Transfer Characteristics

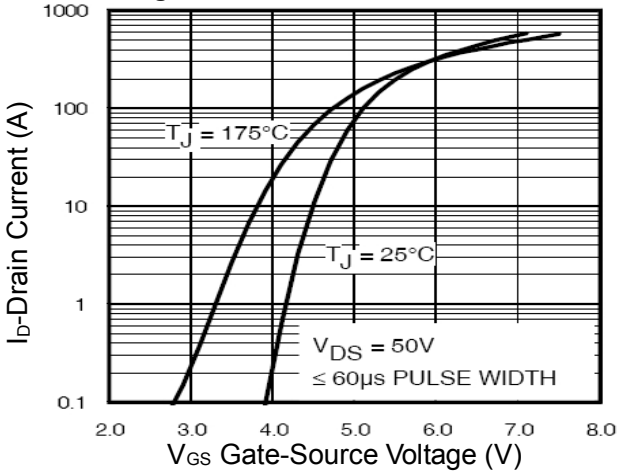


Figure3. $R_{DS(ON)}$- Drain Current

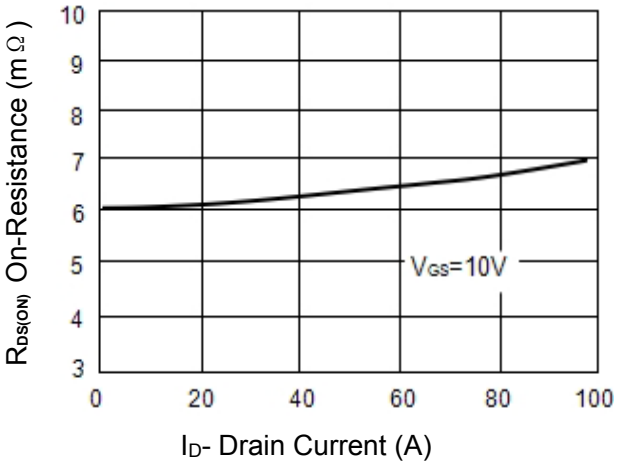


Figure4. $R_{DS(ON)}$- JunctionTemperature

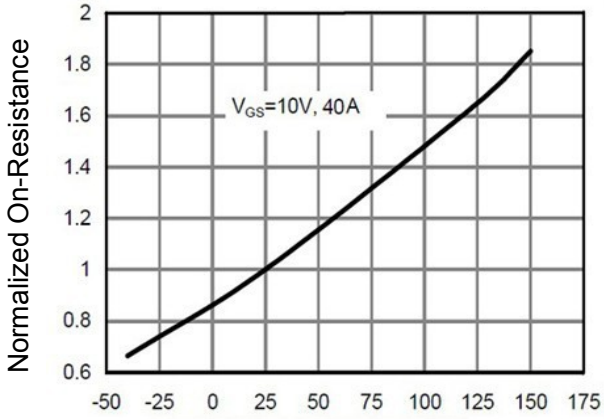


Figure5. Gate Charge

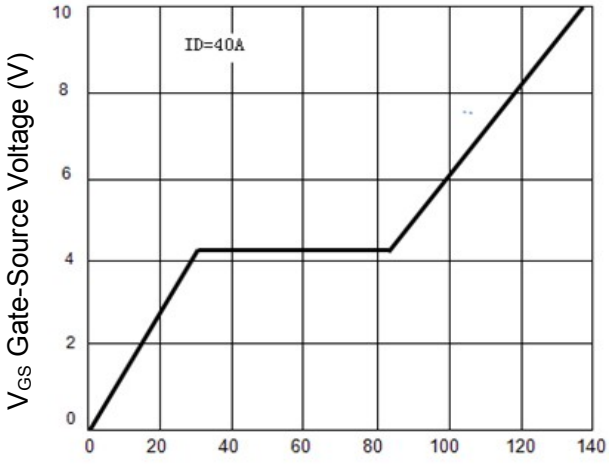


Figure6. Source- Drain Diode Forward

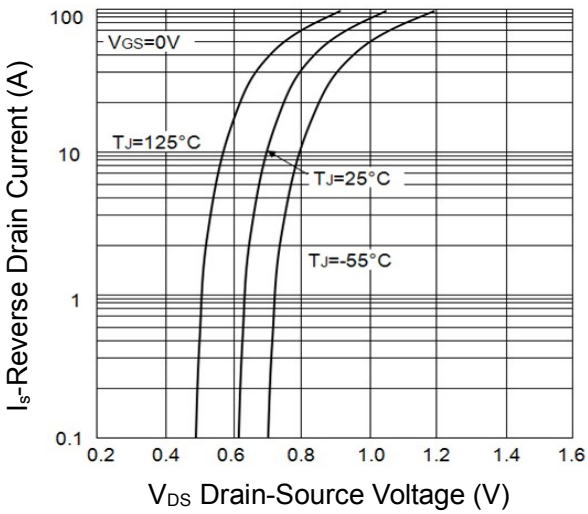


Figure7. Capacitance vs V_{DS}

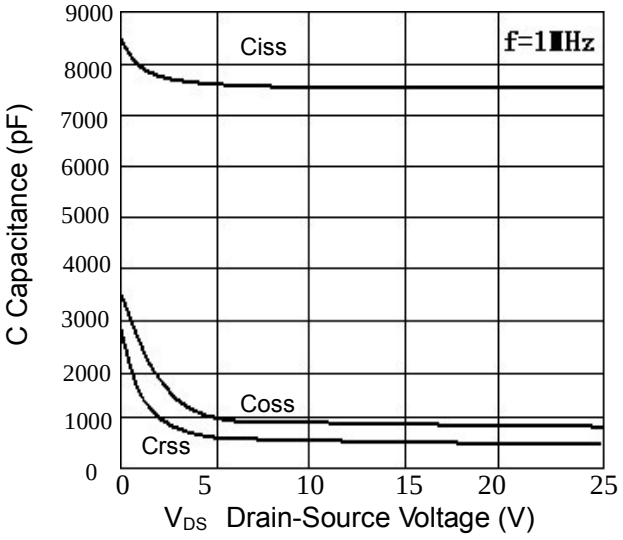


Figure8. Safe Operation Area

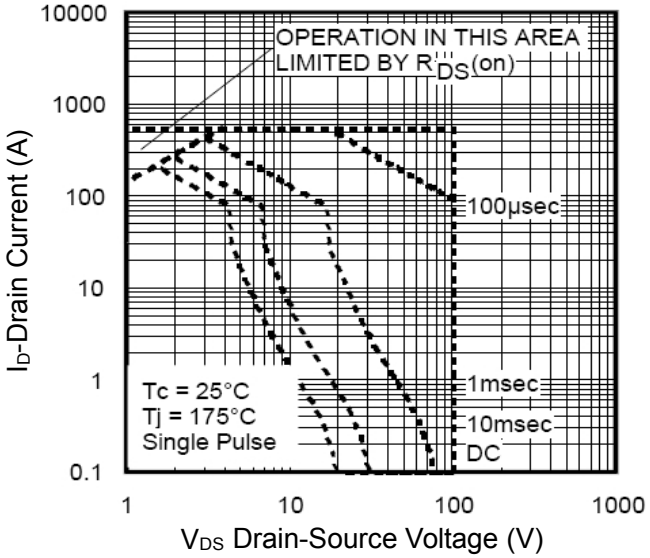


Figure9. BV_{DSS} vs Junction Temperature

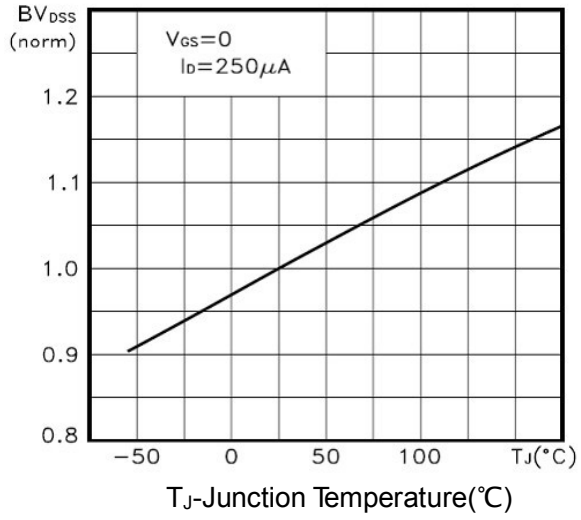


Figure10. VGS(th) vs Junction Temperature

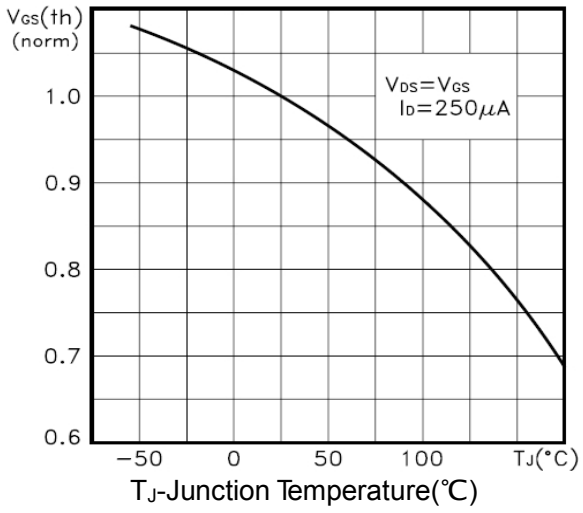
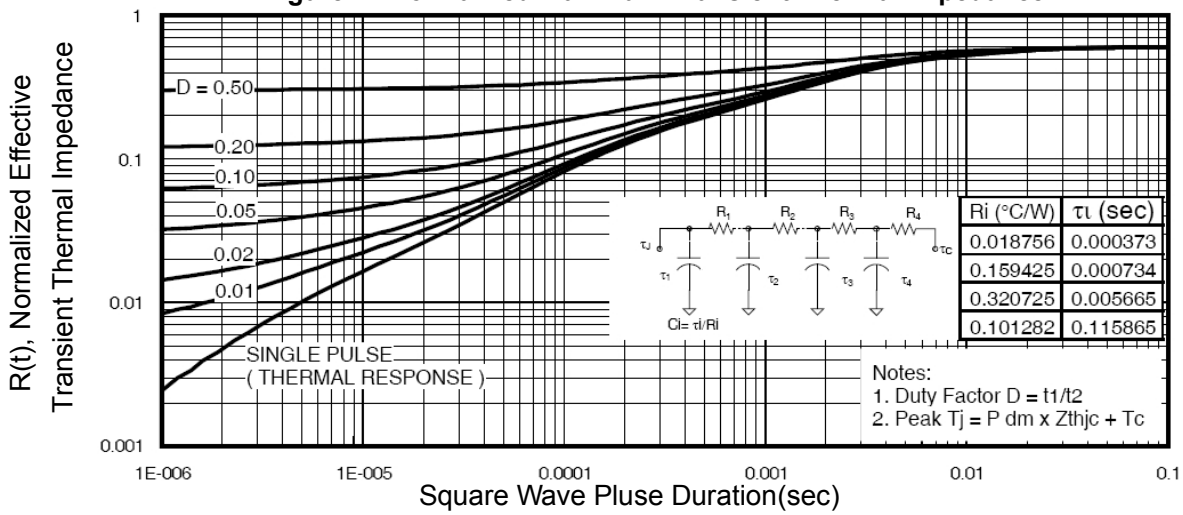
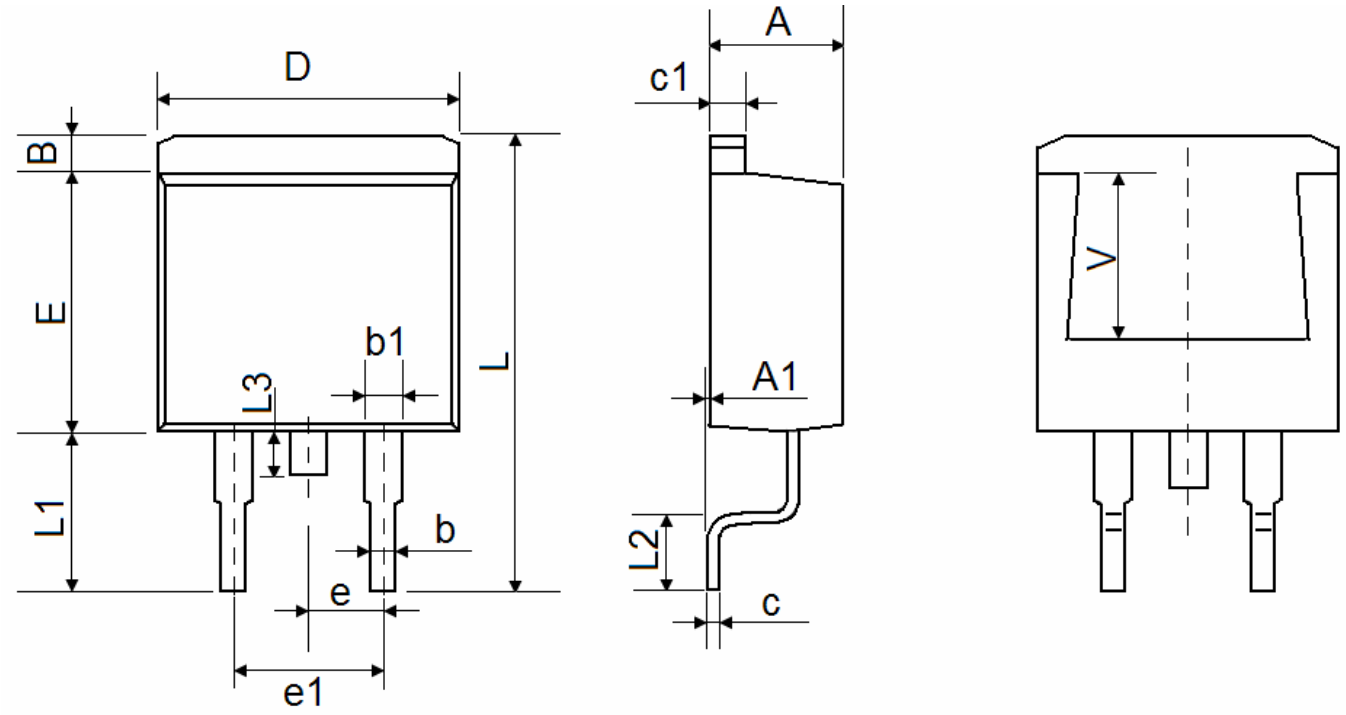


Figure11. Normalized Maximum Transient Thermal Impedance





Symbol				
	Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	